

Power Flow Control in Transmission Line by Using UPFC

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Abstract: The power transfer capacity of electrical transmission lines is typically constrained by the capacity of large signals. Economic considerations such as the high cost of long lines and the income from the supply of additional power make it possible to pursue both economically and technically viable ways to increase the stability cap intensively. On the other hand, the production of efficient ways to use the full thermal capacity of transmission systems. The power industry has already begun to be affected by fast development in the field of power electronics. This is one direct consequence of the idea of aspects of FACTS, which has become possible due to the progress realized in power electronic devices. In theory, the FACTS devices can provide rapid control of real and Var power through transmission line. The UPFC is a FACTS family member with very appealing characteristics. Many parameters can be independently controlled by this unit. An alternative means of minimizing transmission system oscillations is given by this unit. The choice of input signals and the adopted control strategy for this system in order to dampen power oscillations in an efficient and robust manner are an important issue. In order to achieve the maximum desire effect in solving the first swing stability problem, the UPFC parameters can be regulated. In bulky power transmission systems with long transmission lines, this problem arises. A MATLAB Simulink Model with UPFC to test the efficiency of the electrical transmission system is considered in this paper. The main purpose of this analysis paper is to research different studies performed in the past to minimize UPFC transmission line losses.

Keywords: UPFC, FACTS, STATCOM, SSSC.

1. Introduction

As high-power semiconductor technology continues to grow rapidly, it is now we are able to monitor electrical power systems by using electronic power devices. Such equipment is an evolving technology known as FACTS, which (flexible alternating current transmission systems). In 1988, N.G Hingorani presented its first concept (FACTS) as a very common and significant device for power systems. [1]. There are a range of advantages to FACTS technology, such as better control of power flow, it ensures safe loading of current transmission circuits, dampens power system oscillations, has less environmental effects, and is a smaller amount costly than another substitute transmission system reinforcement strategy.

Synchronous Compensator (STATOM), Static Series Capacitor (SCSC), Static Series Compensator (SSSC), Unified

Power Flow Controllers (UPFCs), Thyristor Switched Capacitor (TSC), Thyristor Switched Capacitor (TSC), Thyristor Switched Capacitor (TSC), Thyristor Switched Capacitor (TSC), Thyristor Switched (TSC). Many FACTS-devices have been introduced for different power system applications.

UPFC is the most flexible tool used in FACTS devices. It is not only capable of performing, but it is also willing to do so. The static synchronous compensator (STATCOM), thyristor switched capacitor (TCS), thyristor controlled reactor (TCR), and phase angle regulator functions provide more flexibility by combining several of the above control functions. UPFCs primarily regulate active and VAR power flow by injecting the voltage by series manner into the transmission line, with phase angle and magnitude of voltage being independently variable. Power flow control, both reactive and actual, can allow power to flow on designated routes and load transmission lines near their thermal limits, as well as improve transient and low-power system signal stability.

UPFC integrates two supplementary FACTS devices: STATCOM and SSSC. Both are voltage source inverter (VSI). One is shunt connected and other series connected to transmission line.

2. Unified Power Flow Controller

In today's generation, UPFC is one of the most versatile platforms for multi-functional FACTS. UPFC can be regulated transmitted power by adjusting three power transmission line parameter, namely the impedance, phase angle and voltage of the transmission line.

UPFC is a voltage source converter that combines two voltage source converters, a shunt and a series converter, and connects them through a common dc connection. In addition to providing the dc power provided by both inverters as illustrated in fig. 1, shunt converters (converter-1) or static synchronous compensators (STATCOM) supply reactive power to the ac device, while serial converters (converter-2) or SSSC add the operated tensile immensity range. Every single one of the divisions It consists of an electronic converter for transformers and control. A common dc capacitor was shared by those two voltage source converters. In between the Ac terminal of two

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converters the true power will flow freely in either direction. In this regard, the key feature of converter 2 is to inject the AC voltage V_s at the device frequency with the not fixed magnitude $|V_s|$, ($|V_s| \leq 0 \leq |V_s| \text{ max}$) and the phase angle (which lies between 0 and 2π) in series. Converter-1, on the other side, is primarily used to supply the actual power required by converter-2 by the common dc link.

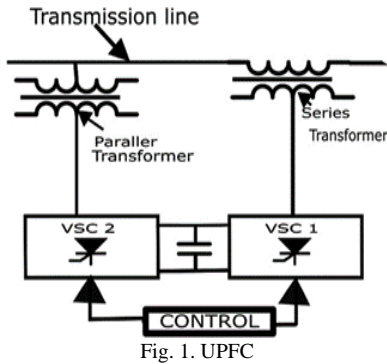


Fig. 1. UPFC

This dc condenser is generally limited in its capacity for storing electricity. You may choose separately the reactive power of series or shunt converter to increase the fluidity of controlling of power flow.

3. UPFC's Voltage Source Converter

A. STATCOM

The term STATCOM refers to as Static VAR Compensator which is shunt connected and its output current can be regulated regardless of the Alternating Current system voltage, whether capacitive or inductive. The AC output voltage of the voltage supply converter is controlled to ensure that there is enough reactive current flow for all AC bus voltages. If required, the voltage supply converter is monitored so that it is appropriate for being necessary flow of reactive current for all voltage of Alternating Current bus.

The voltage of DC capacitor is inevitably balanced to assist as the voltage source of the converter. The aim of STATCOM was to service as an active filter to engross system harmonics. Shown Fig. 2, the diagram for STATCOM beyond of an energy storing unit.

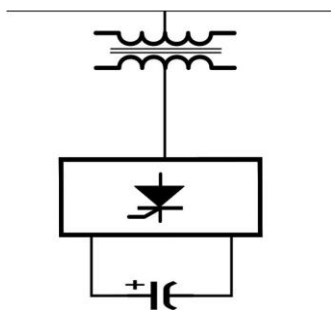


Fig. 2. STATCOM

B. SSSC

The Static Synchronous Series Generator operates in quadrant mode, allowing it to independently increment or

decrement the total VAR voltage drop in distribution line, thus governing the transmitted electric power without the use of an external power source. The SSSC may provide transient energy storage or power absorption devices to improve zestful performance by temporarily increment or decrement the total actual voltage fall along the line.

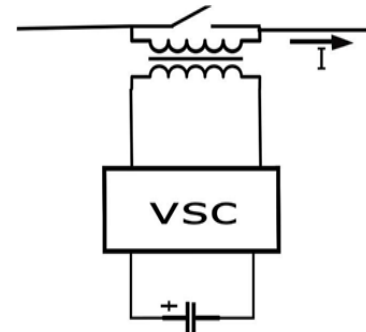


Fig. 3. SSSC

4. Control Unit

A. Shunt Converter Control

Fig. 4, represent the block diagram of shunt converter. A regulated current from the device is drawn by the shunt converter. I_p , which is I_p , is one part of this current Automatically performed by the necessity to match the actual power delivered to the series connected converter across the Direct current connection. DC capacitor voltage controls by response control, this power balance is implemented.

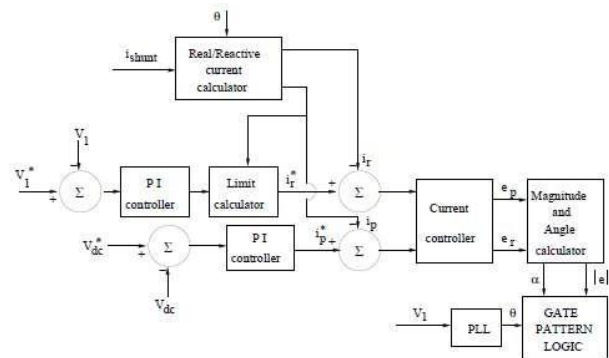


Fig. 4. Control circuit

The other half of the shunt converter current (I_r), which is adjustable in the same way as STATCOM is. This can be set to one of two modes of operation. Such instances are as follows:

Control mode of the VAR in which the inductive or capacitive command specifies the relationship of the reactive present. Current transformer (CT) produces feedback signal and are usually found on the transformer's (stage down) coupling bushings.

Self-voltage control mode, with a feedback voltage controller output deciding the relation to reactive current, including the drop characteristic (in case of a SVC or a STATCOM). Potential transformer (PT) measure V_1 is the voltage at the substation that feeds the transformer and provides voltage feedback.

B. Series converter controls

Fig. 5 shows the block diagram of the converter sequence is illustrated. A serial injected voltage is defined to ensure flow of necessary current (phasor) even when the device is disturbed in this control mode. (The power and reactive power modulation is unless the system control dictates).

Although normal conditions are needed for the instruction of the apparent power flow in the line, eventuality conditions necessitate the use of a control unit to contribute to device constancy by oscillations in damping power.

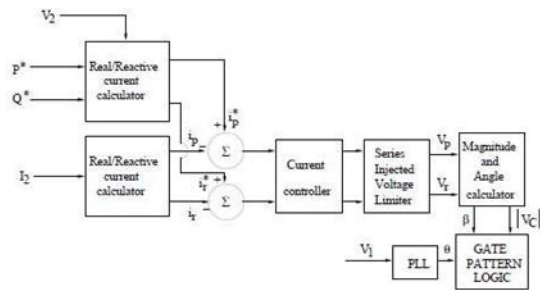


Fig. 5. Series converter control

The following are the various voltage control modes for the series:

1. Direct voltage injection mode in which, according to the reference input, the converter simply generates a voltage phasor. This is a special case where the reactive voltage is the desired voltage and the line current is in quadrature.
2. The injected voltage is phase shifted by an angle indicated by the reference input relative to the voltage in this mode of emulation.
3. Automatic mode of power flow control in which the reference inputs define the active power (P) and VAR (Q) necessary at the appropriate transmission line location.

5. Simulink Modelling

The control and simulation of UPFCs using the MATLAB environment have been addressed in this section. Systems simulation work has been conducted for the Multi Machine (3 machines 9 bus). In this case it has been tested for various responses to the existence of three-phase failure and different system parameters. In the course of the investigation, failure clearing, fault positioning and its impact on system stability lead to knowledge of variance in load angle. The IEEE system, with its 3-machine 9-bus system and standard parameter values, was used as a test case. Angle of phase shifter. Injected voltage is phase shifted by the angle indicated by the reference input relative to the voltage in this mode of emulation. This model does not attach to the compensating system. It gives a temporary system answer in the event of a malfunction. Fig. 6 shows in the IEEE 3-Machine 9-Bus model based on MATLAB/Simulink with UPFC and various phase fault forms between bus 5-7.

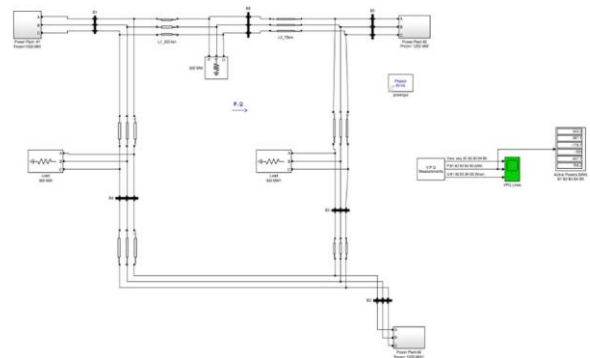


Fig. 6. IEEE Platform 9 bus framework without UPFC MATLAB/Simulink based model

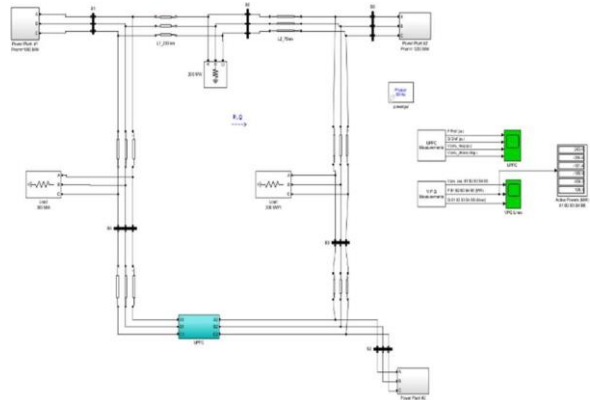


Fig. 7. IEEE Platform 9 bus framework with UPFC MATLAB/Simulink based model

6. Result

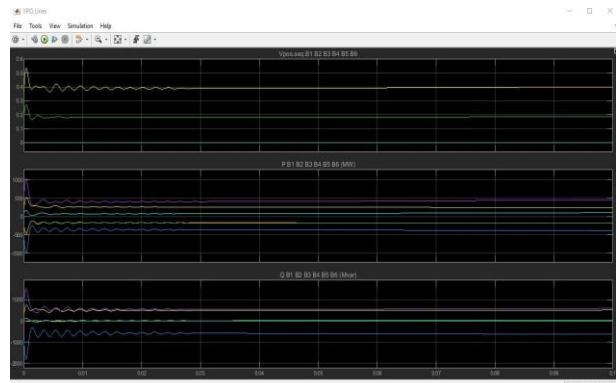


Fig. 8. Voltage profile when UPFC not connected

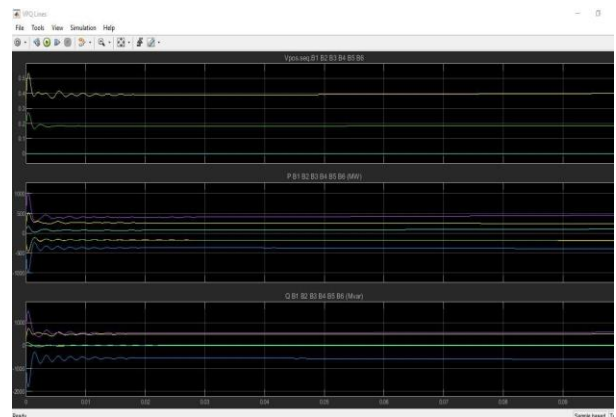


Fig. 9. Voltage profile when UPFC connected

A. Simulation result for power transfer capability

Table 1
Voltage

BUS	WITHOUT UPFC	WITH UPFC
B1	0.3997	0.3997
B2	2.63e-14	4.517e-12
B3	5.086e-15	2.859e-12
B4	0.1858	0.1866
B5	1.719e-14	7.773e-12
B6	1.081e-14	4.259e-12

VOLTAGE (kV)

Table 2
Active power

BUS	WITHOUT UPFC	WITH UPFC
B1	242.2	243.6
B2	-387.1	-394.4
B3	-178.7	-181.4
B4	-183	-185.9
B5	457.7	459.3
B6	108.2	108.3

ACTIVE POWER (MW)

Table 3
Reactive power

BUS	WITHOUT UPFC	WITH UPFC
B1	513.5	512.9
B2	-629.1	-603.5
B3	2.182	1.175
B4	22.23	21.51
B5	588.9	588.5
B6	-2.554	-2.595

REACTIVE POWER (MW)

7. Conclusion

FACTS equipment that is traditionally used in the power system for voltage profile enhancement controls actual and reactive power flow and improves transmission capability. With the aid of load flow, the steady state UPFC injection model is incorporated into the IEEE-9 bus system. It has been discovered that incorporating UPFC into the above device reduces system losses. Each bus's voltage profile has also increased. They plot and compare the bus voltage with and without UPFC.

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